## Amendments to the Claims:

This listing of claims will replace all prior versions and listings of claims in the application:

## **Listing of Claims:**

- 1. (Original) An optoelectronic package comprising: a base chip, the chip comprising a base chip V-groove;
  - a fiber array, the array comprising an array V-groove formed in a rear portion and a front portion of the array;
  - a first wick stop disposed between the rear and front portions; and an optical fiber, having an angled endface, disposed in both V-grooves to substantially couple the chip and array.
- 2. (Original) The optoelectronic package as in claim 1 wherein the array further comprises a sealing lid.
- 3. (Original) The optoelectronic package as in claim 2 wherein the sealing lid comprises a monolithic sealing lid.
- 4. (Currently Amended) The optoelectronic package as in claim 1 wherein the base chip further comprises an etch stop layer in the base chip under the laser.
- 5. (Original) The optoelectronic package as in claim 4 wherein the layer comprises SiO<sub>2</sub>.
- 6. (Currently Amended) The optoelectronic package as in claim 4 wherein the layer comprises silicon <u>nitride</u> nitrate.

7. (Currently Amended) The optoelectronic package as in claim 4 wherein the layer comprises  $\underline{\text{Al}_2\text{O}_3}$   $\underline{\text{AL}_2\text{O}_3}$ .

- 8. (Original) The optoelectronic package as in claim 1 wherein the array comprises a first pit and the chip comprises a second pit.
- 9. (Original) The optoelectronic package as in claim 1 further comprising an alignment sphere, wherein the sphere is disposed between the first and second pits.
- 10. (Original) The optoelectronic package as in claim 1 wherein the array comprises a middle portion bordered by the first wick stop and a second wick stop.
- 11. (Original) The optoelectronic package as in claim 1 wherein the base chip further comprises a surface device.
- 12. (Original) The optoelectronic package as in claim 1 wherein the surface device comprises a VCSEL.
- 13. (Original) The optoelectronic package as in claim 1 wherein the surface device comprises a photodector.
- 14. (Original) A method for coupling optoelectronic packages comprising: forming a base chip V-groove in a base chip;

forming an array V-groove in a rear portion and a front portion of a fiber array;

disposing a first wick stop between the rear and front portions; and

disposing an optical fiber, having an angled endface, in both V-grooves to substantially couple the chip and array.

- 15. (Original) The method as in claim 14 further comprising disposing a surface device on the base chip.
- 16. (Original) The method as in claim 15 wherein the surface device comprises a VCSEL.
- 17. (Original) The method as in claim 15 wherein the surface device comprises a photodector.
- 18. (Original) The method as in claim 14 further comprising enclosing the surface device within a sealing lid.
- 19. (Original) The method as in claim 18 wherein the sealing lid comprises a monolithic sealing lid.
- 20. (Original) The method as in claim 14 further comprising forming an etch stop layer in the base chip.
- 21. (Original) The method as in claim 20 wherein the layer comprises SiO<sub>2</sub>.
- 22. (Currently Amended) The method as in claim 20 wherein the layer comprises silicon <u>nitride</u> nitrate.
- 23. (Currently Amended) The method as in claim 20 wherein the layer comprises Al<sub>2</sub>O<sub>3</sub> AL<sub>2</sub>O<sub>3</sub>.
- 24. (Original) The method as in claim 14 further comprising forming a first pit on the array and a second pit on the base chip.
- 25. (Original) The method as in claim 24 further comprising disposing an alignment sphere between the first and second pits.

26. (Original) The method as in claim 14 further comprising forming a middle portion bordered by the first wick stop and a second wick stop.

27. (New) An optoelectronic package comprising:

a base chip;

a fiber array, the array comprising an array V-groove formed in a rear portion and a front portion of the array;

an optical fiber disposed in the V-groove to substantially couple the chip and array; and

a first wick stop disposed between the rear and front portions effective to prevent adhesive to be applied to the optical fiber on the rear portion of the array from flowing to the front portion of the array during assembly of the package.

28. (New) The optoelectronic package as in claim 27 wherein the array further comprises a sealing lid.